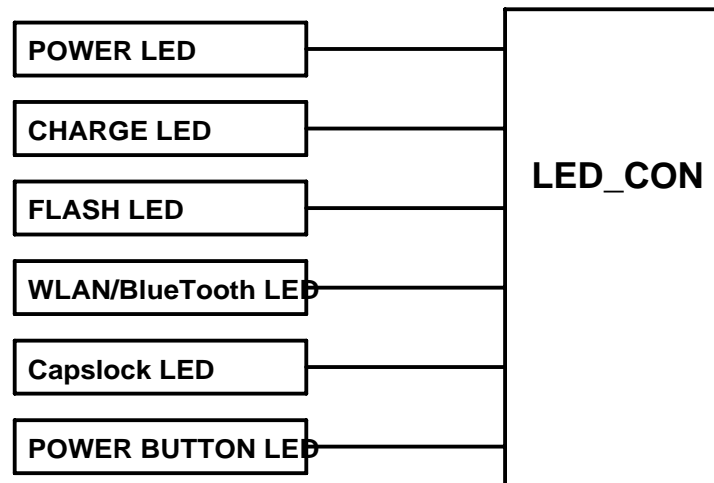


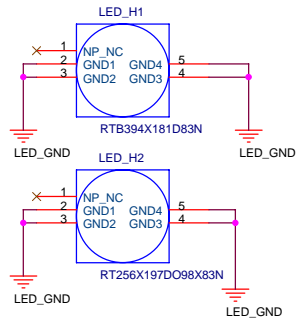
# MK90H\_LED\_BOARD<sub>1.2G</sub>

2009\_0416\_1850

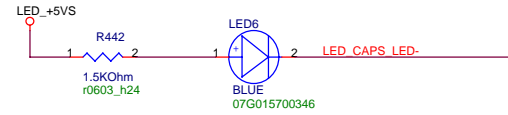


<Core Design>

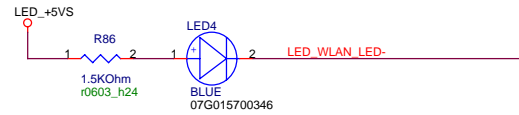
		Title : Block Diagram	
ASUSTek Computer INC.		Engineer: Regin Chiang	
Size	Project Name		Rev
A3	MK90H_LED		1.2G
Date: Thursday, April 16, 2009		Sheet	1 of 3



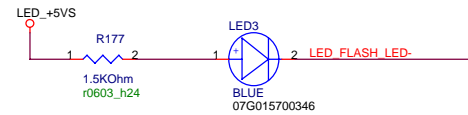
### for Capslock LED



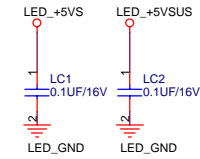
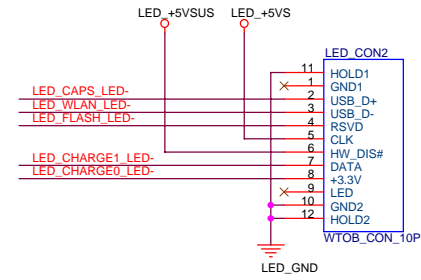
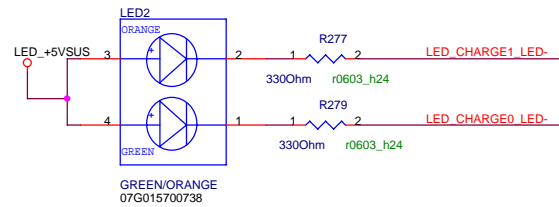
### for WLAN/BlueTooth LED



### for FLASH LED



### for CHARGE LED




<Core Design>

<b>ASUS</b>		Title : <b>LED</b>	
ASUSTek Computer Inc.		Engineer: <b>Kell Huang</b>	
Size A3	Project Name <b>MK90H_LED</b>	Rev 1.2G	
Date: Thursday, April 16, 2009	Sheet	2	of 3



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		Title : History	
ASUSTek Computer INC.		Engineer: Satan He	
Size	Project Name		Rev
A3	MK90H_LED		1.2G
Date: Thursday, April 16, 2009	Sheet	3	of 3